Abstracts

Low cost 900 MHz single-chip cordless telephone receiver

D. Lovelace, S. Bader, D. Coffing, J. Durec, R. Hester, K. Huehne, E. Main, P. Ovalle, M. Randol, R. Tang, D. Welty, R. Williams and K. Wortel. "Low cost 900 MHz single-chip cordless telephone receiver." 2000 Radio Frequency Integrated Circuits (RFIC) Symposium 00. (2000 [RFIC]): 87-90.

A low cost 900 MHz single-chip cordless telephone receiver is presented, This IC is suitable for 900 MHz portable wireless applications including cordless telephone. Receiver functions are programmed through a serial port interface (SPI), The receiver IC can be operated either as a single conversion or dual conversion receiver with a 12 dB SINAD of -115 dBm. Power consumption is rated at 20 mA of supply current operating from 1.8 V to 2.8 V. This IC utilizes a 0.4 /spl mu/m BiCMOS process and features an LNA, two mixers, IF amplifier, IF limiter, demodulator, VCO, crystal reference oscillator, PLL, and an 80 bit SPI. All of these functions have been integrated onto a 1.09 mm/spl times/1.08 mm (1.18 mm/sup 2/) die.

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